

FIG. 1

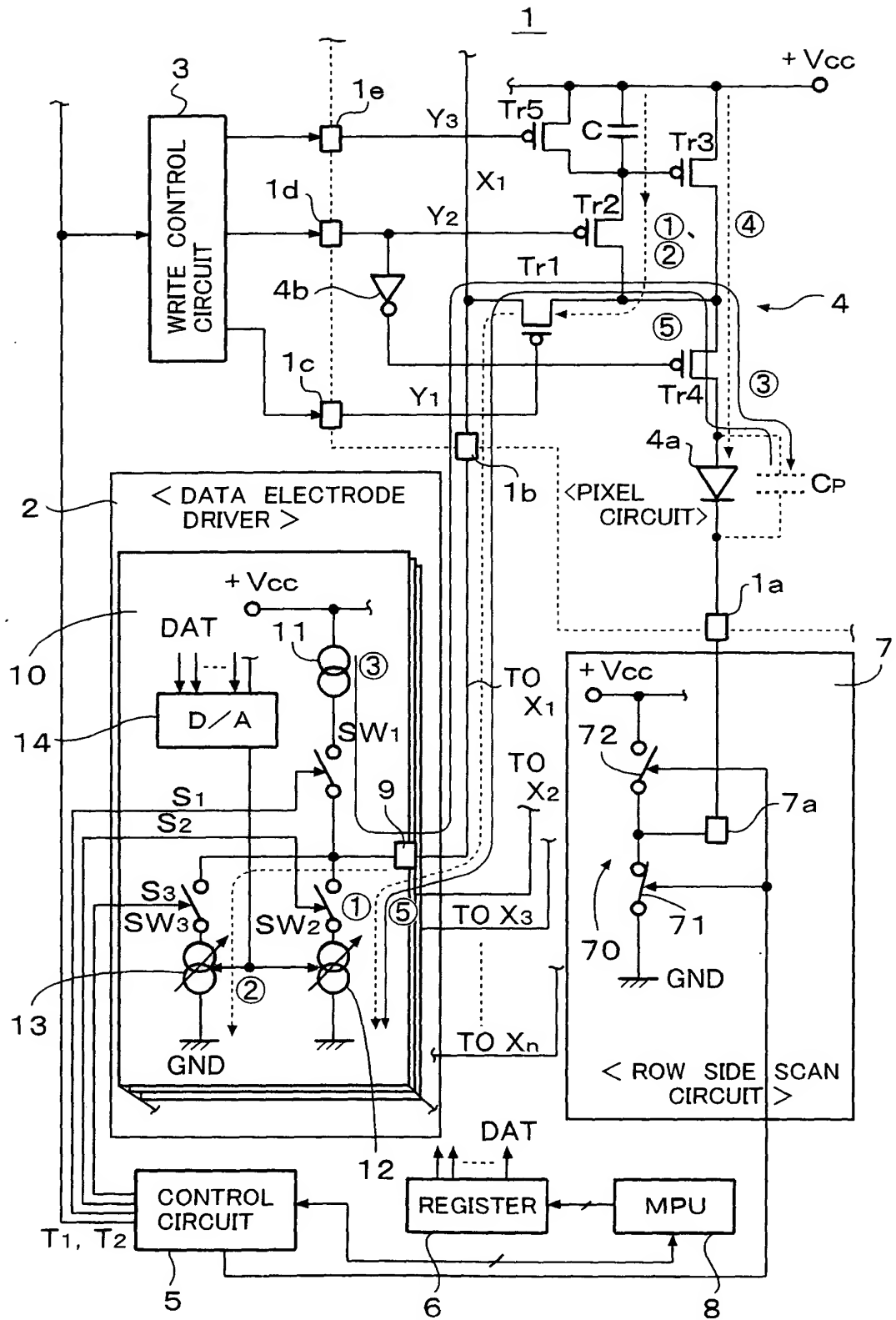


FIG. 2

	SELECTION LINE, ERASE LINE			CONTROL SIGNAL			TRANSISTORS OF PIXEL CIRCUIT					CURRENT PATH
	Y1	Y2	Y3	S1	S2	S3	Tr1	Tr2	Tr3	Tr4	Tr5	
INITIAL CHARGE OF CAPACITOR C	"L"	"L"	"H"	"L"	"H"	"H"	ON	ON	ON	OFF	OFF	①
VOLTAGE WRITE IN CAPACITOR C	"L"	"L"	"H"	"L"	"L"	"H"	ON	ON	ON	OFF	OFF	②
INITIAL CHARGE OF OEL ELEMENT	"L"	"H"	"H"	"H"	"L"	"L"	ON	OFF	ON	ON	OFF	③
LIGHT EMITTING DRIVE OF OEL ELEMENT	"H"	"H"	"H"	"L"	"L"	"L"	OFF	OFF	ON	ON	OFF	④
RESETTING OF CAPACITOR C AND OEL ELEMENT	"L"	"H"	"L"	"L"	"H"	"L"	ON	OFF	OFF	ON	ON	⑤

FIG. 3

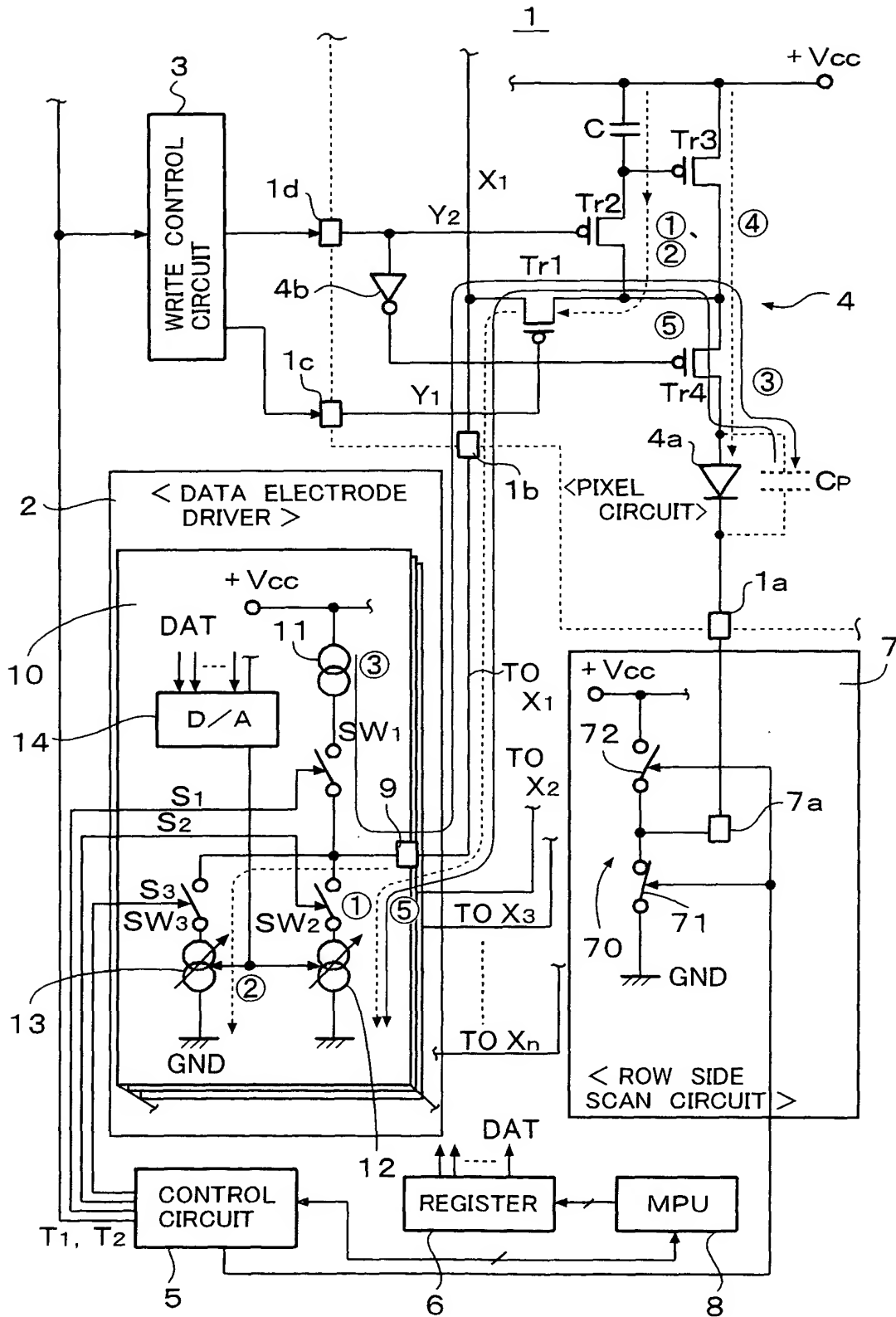


FIG. 4

